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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/925,889	08/06/2001	Rasekh Rifaat	A0312/7412 WRM/IB	6192
23628 75	590 03/29/2005		EXAM	INER
	NFIELD & SACKS, PC	BURD, KEVIN MICHAEL		
FEDERAL RESERVE PLAZA 600 ATLANTIC AVENUE BOSTON, MA 02210-2211			ART UNIT	PAPER NUMBER
			2631	
			DATE MAILED: 03/29/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/925,889	RIFAAT ET AL.				
Office Action Summary	Examiner	Art Unit				
	Kevin M. Burd	2631				
The MAILING DATE of this communication Period for Reply	n appears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR RITHE MAILING DATE OF THIS COMMUNICATION Extensions of time may be available under the provisions of 37 Cf after SIX (6) MONTHS from the mailing date of this communication of the period for reply specified above is less than thirty (30) days, of the period for reply is specified above, the maximum statutory properties of the period for reply within the set or extended period for reply will, by any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	ON. FR 1.136(a). In no event, however, may a on. a reply within the statutory minimum of thieriod will apply and will expire SIX (6) MO statute, cause the application to become A	reply be timely filed  rty (30) days will be considered timely.  NTHS from the mailing date of this communication.  BANDONED (35 U.S.C. § 133).				
Status						
1)⊠ Responsive to communication(s) filed on	08 November 2004.					
	This action is non-final.					
	Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) 1-14 and 16-30 is/are pending in 4a) Of the above claim(s) is/are with 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-14 and 16-30 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction a	ndrawn from consideration.					
Application Papers						
9) The specification is objected to by the Exa	miner.					
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
Applicant may not request that any objection to	the drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of:  1. Certified copies of the priority docur 2. Certified copies of the priority docur 3. Copies of the certified copies of the application from the International But * See the attached detailed Office action for a	ments have been received. ments have been received in A priority documents have beer ureau (PCT Rule 17.2(a)).	Application No  received in this National Stage				
Attachment(s)	_					
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-9483)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SI Paper No(s)/Mail Date</li> </ol>	Paper No(	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152) 				

Application/Control Number: 09/925,889 Page 2

Art Unit: 2631

1. This office action, in response to the remarks filed 11/8/2004, is a non-final office

action.

Response to Arguments

2. Applicant's arguments regarding claims 26-30 filed 11/8/2004 have been fully

considered but they are not persuasive. Applicant states in the remarks filed 11/8/2004

on pages 2-3, Ozluturk does not disclose the claimed actions are executed in response

to a single instruction. The input signal is the single instruction in the rejection and the

subsequent steps of the method are executed in response to this instruction. In addition,

the complex multiplication is carried out in a single clock cycle and is added and latched

out in a subsequent clock cycle as stated in the previous rejection. Claims 27-30 claim

only the complex multiplication is carried out in a single clock cycle. The rejection of

these claims is maintained.

3. Applicant's arguments on pages 2 and 3 regarding claims 1-14 and 16-26 filed

11/8/2004 have been fully considered and are persuasive. The rejection of these claims

is withdrawn. However, a new rejection of these claims is found below in view of Dent

(US 6,680,928).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1-14, 16-30 are rejected under 35 U.S.C. 102(e) as being anticipated by Dent (US 6,680,928).

Regarding claims 1, 8, 16 and 27-30, Dent discloses a method of processing received signal values in a signal processor (figures 8 and 9). A digital spread spectrum signal is input to a rake receiver in figure 8. The signal has a plurality of signal values and the signal values are input to complex mixers 146. Each multiplication product is input into an accumulator 148, where it is added to other channels. Despreading occurs in block 112. The multiplied outputs are combined and a complex signal is developed for each coded information symbol period (column 15, lines 26-34).

Regarding claims 2 and 9, the output of the combination is the despread product.

Regarding claims 3, 17 and 10, the PN code used for dispreading is input to the mixers as shown in figure 9. This code can be divided by a factor of four to yield one-fourth the amplitude as can any despread code.

Regarding claims 4, 11 and 18, the PN code used for despreading the signal as shown in figure 9, will comprise a plurality of bits. The signal is a complex signal and will comprise at least one real and at least one imaginary bit.

Regarding claims 5, 12 and 19, the received signal comprises values of "1" and "-1" (column 6, lines 37-43).

Art Unit: 2631

Regarding claims 6, 13 and 20, the communication system discloses one or more coded information bits (column 11, lines 30-41).

Regarding claims 7, 14 and 21, the received signal comprises values of "1" and "-1" (column 6, lines 37-43).

Regarding claims 22-25, Dent discloses a method of processing received signal values in a signal processor (figures 8 and 9). A digital spread spectrum signal is input to a rake receiver in figure 8. The signal has a plurality of signal values and the signal values are input to complex mixers 146. Each multiplication product is input into an accumulator 148, where it is added to other channels. Despreading occurs in block 112. The multiplied outputs are combined and a complex signal is developed for each coded information symbol period (column 15, lines 26-34). The communication system discloses one or more coded information bits (column 11, lines 30-41). The received signal comprises values of "1" and "-1" (column 6, lines 37-43).

Regarding claim 26, the data communication system is a CDMA system (column 2, lines 52-54).

5. Claims 27-30 are rejected under 35 U.S.C. 102(e) as being anticipated by Ozluturk et al (US 6,366,607).

Regarding claim 27, Ozluturk discloses a method of processing received signal values in a signal processor. A digital spread spectrum signal is input to a rake receiver 101 in figure 5. The signal has a plurality of signal values and the signal values are input to complex mixers 107. The PN code input to the mixers will comprise a plurality of bits.

Art Unit: 2631

Each multiplication product is input into an accumulator 109, where it is added to a previous product and latched out after the next symbol clock cycle (column 4, lines 59-67).

Regarding claims 28-30, Ozluturk discloses a method of processing received signal values in a signal processor. A digital spread spectrum signal is input to a rake receiver 101 in figure 5. The signal has a plurality of signal values and the signal values are input to complex mixers 107. The PN code input to the mixers will comprise a plurality of bits. Each multiplication product is input into an accumulator 109, where it is added to a previous product and latched out after the next symbol clock cycle (column 4, lines 59-67) thereby despreading the signal. A plurality of code segments is provided to the receiver to despread the received signal values as shown in figure 5.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Burd whose telephone number is (571) 272-3008. The examiner can normally be reached on Monday - Thursday 9 am - 5 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on (571) 272-3021. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2631

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kevin M. Burd 3/19/2005

> KEVIN BURD PRIMARY EXAMINER